

# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR



ANANTAPUR - 515 002(A. P.) INDIA

EXAMINATION BRANCH

B Tech II Year I (R09) Semester Supplementary Examinations May 2012 Results

SUBCODE	SUBNAME	I.M	E.M	TOTAL	RESULT	CREDITS
<b>093M1A0228</b>	<b>ORSU SWATHI</b>					
9A02305	ELECTRICAL CIRCUITS	29	12	41	F	0
<b>093M1A0241</b>	<b>VALLEPU DIVYA</b>					
9A02308	ELECTRICAL MACHINCES-I	22	10	32	F	0
9ABS302	MATHEMATICS-III	28	8	36	F	0
<b>093M1A0408</b>	<b>BOGGALA VIDYA SADHANA</b>					
9A04304	SIGNALS & SYSTEMS	29	AB	29	F	0
<b>093M1A0410</b>	<b>CHAPPIDI SUPRAJA</b>					
9A04304	SIGNALS & SYSTEMS	28	10	38	F	0
<b>093M1A0417</b>	<b>GADDA AFROZE BANU</b>					
9A02305	ELECTRICAL CIRCUITS	24	8	32	F	0
<b>093M1A0456</b>	<b>RANGAGARI SULOCHANA</b>					
9A04304	SIGNALS & SYSTEMS	28	10	38	F	0
<b>093M1A0458</b>	<b>SAKUNALA NAVYA</b>					
9A04304	SIGNALS & SYSTEMS	28	10	38	F	0
<b>093M1A0463</b>	<b>VEERBALLI GANGA DEVI</b>					
9A04303	PROBABILITY THEORY & STOCHASTIC PROCESSES	19	AB	19	F	0
9A04304	SIGNALS & SYSTEMS	27	4	31	F	0
<b>093M1A0515</b>	<b>DUDEKULA ASHABEE</b>					
9A04301	ELECTRONIC DEVICES & CIRCUITS	23	44	67	P	4
<b>093M1A0522</b>	<b>KADAPA DEEPIKA</b>					
9A04306	DIGITAL LOGIC DESIGN	29	4	33	F	0
9A04301	ELECTRONIC DEVICES & CIRCUITS	23	25	48	P	4
<b>093M1A0529</b>	<b>KOTHAPU GEERVANI</b>					
9A02306	BASIC ELECTRICAL ENGINEERING	23	10	33	F	0
<b>093M1A0534</b>	<b>MUDE JYOTHIRMAI</b>					
9ABS304	PROBABILITY & STATISTICS	23	8	31	F	0
9A02306	BASIC ELECTRICAL ENGINEERING	16	13	29	F	0
<b>093M1A0548</b>	<b>PEDDAGOLLA RAMALAKSHMI</b>					
9A02306	BASIC ELECTRICAL ENGINEERING	21	17	38	F	0
<b>093M1A0558</b>	<b>TARIGOPULA PURNIMA</b>					
9A04301	ELECTRONIC DEVICES & CIRCUITS	21	27	48	P	4
<b>093M1A1206</b>	<b>CHATAKONDU VIJAYALAKSHMI</b>					
9A02306	BASIC ELECTRICAL ENGINEERING	19	15	34	F	0
<b>103M1A0203</b>	<b>GADIKOTA PRASHANTI</b>					
9A04301	ELECTRONIC DEVICES & CIRCUITS	22	48	70	P	4
<b>103M1A0204</b>	<b>GATTUPALLE SIVA KUMAR</b>					
9A02305	ELECTRICAL CIRCUITS	16	25	41	P	4

*E. Krishna Reddy*

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9A02308	ELECTRICAL MACHINES-I	24	12	36	F	0
9ABS302	MATHEMATICS-III	26	7	33	F	0
<b>103M1A0205</b>	<b>GOVUGARI GAYATHRI</b>					
9ABS302	MATHEMATICS-III	25	7	32	F	0
<b>103M1A0402</b>	<b>ANKIREDDYPALLI LAKSHMIPRASANNA</b>					
9A04304	SIGNALS & SYSTEMS	23	12	35	F	0
9A02305	ELECTRICAL CIRCUITS	21	35	56	P	4
<b>103M1A0405</b>	<b>BASANABOYINA SUMALATHA</b>					
9A04304	SIGNALS & SYSTEMS	22	26	48	P	4
<b>103M1A0408</b>	<b>BODICHERLA VIJAYA LAKSHMI</b>					
9A02305	ELECTRICAL CIRCUITS	11	39	50	P	4
<b>103M1A0412</b>	<b>BUDDAREDDIGARI SUNITHA</b>					
9A02305	ELECTRICAL CIRCUITS	20	29	49	P	4
9A04301	ELECTRONIC DEVICES & CIRCUITS	21	36	57	P	4
<b>103M1A0413</b>	<b>CHALUCHALAMALA LAKSHMI MANOGNA</b>					
9A04303	PROBABILITY THEORY & STOCHASTIC PROCESSES	24	25	49	P	4
9ABS302	MATHEMATICS-III	16	1	17	F	0
<b>103M1A0414</b>	<b>CHUKKA LAVANYA</b>					
9A04303	PROBABILITY THEORY & STOCHASTIC PROCESSES	28	25	53	P	4
<b>103M1A0417</b>	<b>ENUGOLU ASWANI</b>					
9A04304	SIGNALS & SYSTEMS	22	45	67	P	4
<b>103M1A0419</b>	<b>GADE SRAVANI</b>					
9A02305	ELECTRICAL CIRCUITS	17	33	50	P	4
9A04304	SIGNALS & SYSTEMS	18	13	31	F	0
<b>103M1A0428</b>	<b>KATABATHINA JAYA LAKSHMI</b>					
9A02305	ELECTRICAL CIRCUITS	18	34	52	P	4
<b>103M1A0431</b>	<b>KHAMMAM JAYALALITHA</b>					
9A04304	SIGNALS & SYSTEMS	24	29	53	P	4
<b>103M1A0433</b>	<b>KODIKUTHALA ISWARYA</b>					
9A04301	ELECTRONIC DEVICES & CIRCUITS	11	29	40	P	4
9ABS302	MATHEMATICS-III	25	8	33	F	0
9A04303	PROBABILITY THEORY & STOCHASTIC PROCESSES	17	25	42	P	4
9A04304	SIGNALS & SYSTEMS	19	10	29	F	0
<b>103M1A0435</b>	<b>KONAKI AMALA</b>					
9A04303	PROBABILITY THEORY & STOCHASTIC PROCESSES	28	26	54	P	4
9A04304	SIGNALS & SYSTEMS	20	31	51	P	4
<b>103M1A0438</b>	<b>LAKSHMAPURAM SUREKHA</b>					

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SUBCODE	SUBNAME	I.M	E.M	TOTAL	RESULT	CREDITS
9A04303	PROBABILITY THEORY & STOCHASTIC PROCESSES	27	25	52	P	4
<b>103M1A0439</b>	<b>LAVADI SREEVANI</b>					
9A02305	ELECTRICAL CIRCUITS	10	33	43	P	4
9A04301	ELECTRONIC DEVICES & CIRCUITS	24	50	74	P	4
9A04304	SIGNALS & SYSTEMS	19	15	34	F	0
<b>103M1A0441</b>	<b>MADDENENI TRIVENI</b>					
9A04301	ELECTRONIC DEVICES & CIRCUITS	24	44	68	P	4
9A04303	PROBABILITY THEORY & STOCHASTIC PROCESSES	17	9	26	F	0
9A04304	SIGNALS & SYSTEMS	20	25	45	P	4
<b>103M1A0443</b>	<b>MANNURU ANUSHA</b>					
9A04304	SIGNALS & SYSTEMS	21	31	52	P	4
<b>103M1A0445</b>	<b>NAGELLA RADHIKA</b>					
9A04301	ELECTRONIC DEVICES & CIRCUITS	14	AB	14	F	0
9ABS302	MATHEMATICS-III	11	AB	11	F	0
9A04303	PROBABILITY THEORY & STOCHASTIC PROCESSES	22	AB	22	F	0
<b>103M1A0447</b>	<b>NALLU GEETHANJALI</b>					
9A04304	SIGNALS & SYSTEMS	20	33	53	P	4
<b>103M1A0450</b>	<b>NELATURI RAMADEVI</b>					
9A04301	ELECTRONIC DEVICES & CIRCUITS	19	35	54	P	4
9A04303	PROBABILITY THEORY & STOCHASTIC PROCESSES	24	6	30	F	0
9A04304	SIGNALS & SYSTEMS	18	7	25	F	0
<b>103M1A0451</b>	<b>OLETI SOWJANYA</b>					
9A04304	SIGNALS & SYSTEMS	23	29	52	P	4
<b>103M1A0452</b>	<b>P MOUNIKA</b>					
9A02305	ELECTRICAL CIRCUITS	11	4	15	F	0
9A04301	ELECTRONIC DEVICES & CIRCUITS	18	44	62	P	4
9A04304	SIGNALS & SYSTEMS	17	2	19	F	0
9ABS302	MATHEMATICS-III	16	2	18	F	0
<b>103M1A0457</b>	<b>PEDDINENI GAYATHRI</b>					
9A04304	SIGNALS & SYSTEMS	23	37	60	P	4
<b>103M1A0460</b>	<b>PIDUGU SRAVANI</b>					
9ABS302	MATHEMATICS-III	21	3	24	F	0
<b>103M1A0462</b>	<b>PURAM REVATHI</b>					
9A04304	SIGNALS & SYSTEMS	19	AB	19	F	0
9ABS302	MATHEMATICS-III	26	4	30	F	0
9A04303	PROBABILITY THEORY & STOCHASTIC PROCESSES	13	27	40	P	4
<b>103M1A0464</b>	<b>RALLAPALLE GEETHA</b>					

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9A04304	SIGNALS & SYSTEMS	22	29	51	P	4
<b>103M1A0466</b>	<b>REVURU SIRISHA</b>					
9A04304	SIGNALS & SYSTEMS	18	29	47	P	4
9ABS302	MATHEMATICS-III	29	8	37	F	0
<b>103M1A0471</b>	<b>SHAIK FARIDA</b>					
9A04304	SIGNALS & SYSTEMS	21	25	46	P	4
<b>103M1A0474</b>	<b>SIDDU LAKSHMINARAYANAMMA</b>					
9A04304	SIGNALS & SYSTEMS	22	32	54	P	4
<b>103M1A0482</b>	<b>VANAKURI SUCHITHRA</b>					
9A04304	SIGNALS & SYSTEMS	22	35	57	P	4
<b>103M1A0485</b>	<b>YEDDULA SREELATHA</b>					
9A04303	PROBABILITY THEORY & STOCHASTIC PROCESSES	28	AB	28	F	0
<b>103M1A0487</b>	<b>YERABALU SIREESHA</b>					
9A04301	ELECTRONIC DEVICES & CIRCUITS	25	56	81	P	4
<b>103M1A0489</b>	<b>DEVU MAMATHA</b>					
9A02305	ELECTRICAL CIRCUITS	9	33	42	P	4
9ABS302	MATHEMATICS-III	21	9	30	F	0
9A04304	SIGNALS & SYSTEMS	16	25	41	P	4
<b>103M1A0501</b>	<b>A KALPANA</b>					
9ABS304	PROBABILITY & STATISTICS	15	2	17	F	0
<b>103M1A0504</b>	<b>CHAVANA HARSHAPRIYA</b>					
9A04301	ELECTRONIC DEVICES & CIRCUITS	19	25	44	P	4
9A04306	DIGITAL LOGIC DESIGN	16	14	30	F	0
9A05301	MATHEMATICAL FOUNDATIONS OF COMPUTER SCIENCE	19	7	26	F	0
<b>103M1A0506</b>	<b>GUNA PRASANTHI</b>					
9ABS304	PROBABILITY & STATISTICS	27	13	40	F	0
<b>103M1A0508</b>	<b>INDLURU NAVYA</b>					
9A02306	BASIC ELECTRICAL ENGINEERING	20	11	31	F	0
<b>103M1A0509</b>	<b>KALI MADHAVI</b>					
9A02306	BASIC ELECTRICAL ENGINEERING	25	30	55	P	4
9ABS304	PROBABILITY & STATISTICS	26	25	51	P	4
<b>103M1A0510</b>	<b>KALLURU SOWMYA</b>					
9A04301	ELECTRONIC DEVICES & CIRCUITS	19	28	47	P	4
<b>103M1A0512</b>	<b>KONDURU SOWMYA</b>					
9A04301	ELECTRONIC DEVICES & CIRCUITS	18	52	70	P	4
<b>103M1A0516</b>	<b>NAGOLU VINUSHA</b>					
9A02306	BASIC ELECTRICAL ENGINEERING	21	25	46	P	4

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9A05301	MATHEMATICAL FOUNDATIONS OF COMPUTER SCIENCE	28	17	45	F	0
<b>103M1A0519</b>	<b>PATTURI PRASANTHI</b>					
9A04301	ELECTRONIC DEVICES & CIRCUITS	18	49	67	P	4
9A02306	BASIC ELECTRICAL ENGINEERING	26	12	38	F	0
<b>103M1A0522</b>	<b>REDYAM MOUNIKA</b>					
9A04301	ELECTRONIC DEVICES & CIRCUITS	25	25	50	P	4
9A04306	DIGITAL LOGIC DESIGN	17	25	42	P	4
<b>103M1A0524</b>	<b>SHAIK REHANA</b>					
9A02306	BASIC ELECTRICAL ENGINEERING	18	8	26	F	0
<b>103M1A0525</b>	<b>SHAIK SHARIFABEGUM</b>					
9ABS304	PROBABILITY & STATISTICS	26	13	39	F	0
<b>103M1A0526</b>	<b>SINGASANI HARSHA NANDINI</b>					
9ABS304	PROBABILITY & STATISTICS	22	26	48	P	4
<b>103M1A0528</b>	<b>VAKATI PRAVALLIKA</b>					
9A02306	BASIC ELECTRICAL ENGINEERING	14	4	18	F	0
9A05302	ADVANCED DATA STRUCTURES	16	25	41	P	4
9ABS304	PROBABILITY & STATISTICS	21	7	28	F	0
9A02307	ELECTRICAL & ELECTRONICS LAB	13	38	51	P	2
<b>103M1A0529</b>	<b>VUNDELA BHAGYALAKSHMI</b>					
9ABS304	PROBABILITY & STATISTICS	28	27	55	P	4
<b>103M1A0530</b>	<b>Y TEJESHVINI VENKATESWARLU</b>					
9A04306	DIGITAL LOGIC DESIGN	20	25	45	P	4

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