

# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

ANANTAPUR - 515 002(A. P.) INDIA



EXAMINATION BRANCH

M Tech I Semester Regular and Supplementary Examinations - April/May 2012 Results

SUBCODE	SUB NAME	I.M	E.M	TOTAL	RESULT
<b>113M1D5501</b>	<b>BAYYAKKAGARI DIVYA</b>				
9D06102	EMBEDDED SYSTEM CONCEPTS	35	40	75	P
9D06103	ADVANCED COMPUTER ARCHITECTURE	38	32	70	P
9D55101	ADVANCED DSP & APPLICATIONS	38	31	69	P
9D55104	MICRO CONTROLLERS & INTERFACING	37	42	79	P
9D55105	OPERATING SYSTEMS	36	33	69	P
9D55107	MICROCONTROLLERS & INTERFACING LAB	38	57	95	P
9D57101	VLSI TECHNOLOGY	39	38	77	P
<b>113M1D5502</b>	<b>MADHURI SADA</b>				
9D06102	EMBEDDED SYSTEM CONCEPTS	38	31	69	P
9D06103	ADVANCED COMPUTER ARCHITECTURE	37	36	73	P
9D55101	ADVANCED DSP & APPLICATIONS	37	36	73	P
9D55104	MICRO CONTROLLERS & INTERFACING	38	35	73	P
9D55105	OPERATING SYSTEMS	36	32	68	P
9D55107	MICROCONTROLLERS & INTERFACING LAB	37	55	92	P
9D57101	VLSI TECHNOLOGY	38	45	83	P
<b>113M1D5503</b>	<b>MADHULATHA KOTHAPU</b>				
9D06102	EMBEDDED SYSTEM CONCEPTS	36	AB	36	F
9D06103	ADVANCED COMPUTER ARCHITECTURE	37	AB	37	F
9D55101	ADVANCED DSP & APPLICATIONS	36	AB	36	F
9D55104	MICRO CONTROLLERS & INTERFACING	38	AB	38	F
9D55105	OPERATING SYSTEMS	35	AB	35	F
9D55107	MICROCONTROLLERS & INTERFACING LAB	37	55	92	P
9D57101	VLSI TECHNOLOGY	39	AB	39	F
<b>113M1D5504</b>	<b>MODUGULA SREEJA</b>				
9D06102	EMBEDDED SYSTEM CONCEPTS	39	27	66	P
9D06103	ADVANCED COMPUTER ARCHITECTURE	39	29	68	P
9D55101	ADVANCED DSP & APPLICATIONS	40	33	73	P
9D55104	MICRO CONTROLLERS & INTERFACING	40	39	79	P
9D55105	OPERATING SYSTEMS	39	28	67	P
9D55107	MICROCONTROLLERS & INTERFACING LAB	40	59	99	P
9D57101	VLSI TECHNOLOGY	39	29	68	P
<b>113M1D5505</b>	<b>RENUKA AMBARAPU</b>				
9D06102	EMBEDDED SYSTEM CONCEPTS	0	AB	0	F
9D06103	ADVANCED COMPUTER ARCHITECTURE	0	AB	0	F
9D55101	ADVANCED DSP & APPLICATIONS	33	AB	33	F

*E. Krishna Reddy*

CONTROLLER OF EXAMINATIONS i/c

Monday, July 09, 2012

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Note: Any discrepancy in the result noted above must be brought to the notice of the Controller of Examinations, within two weeks from the above date

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SUBCODE	SUB NAME	I.M	E.M	TOTAL	RESULT
9D55104	MICRO CONTROLLERS & INTERFACING	0	AB	0	F
9D55105	OPERTING SYSTEMS	35	AB	35	F
9D55107	MICROCONTROLLERS & INTERFACING LAB	34	AB	34	F
9D57101	VLSI TECHNOLOGY	0	AB	0	F

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